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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Application Number	
		Filing Date	
		First Named Inventor	DAVID H. SHEN
		Art Unit	
		Examiner Name	
		Attorney Docket Number	

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

**Examiner
Signature**

Tan Mai

Date Considered

04/10/2007

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/TM/	1	OPPENHEIM et. al., "Discrete-Time Signal Processing," Prentice Hall, USA, 1989, pp. 300-328.	
/TM/	2	GREGORIAN et. al., "Switched-Capacitor Circuit Design," Proceedings of IEEE, vol. 71, no. 8, USA, pp. 941-966, Aug. 1983.	
/TM/	3	CASTELLO et. al., "Performance Limitations in Switched-Capacitor Filters, "IEEE Transactions on Circuits and Systems, vol. CAS-32,no. 9, USA,	
		pp. 865-876, Sept. 1985.	
/TM/	4	MARTIN et. al., "Effects of the Op Amp Finite Gain and Bandwidth on the Performance of Switched-Capacitor Filters," IEEE Transactions on Circuits and Systems, vol. CAS-28, no. 8, USA, pp. 822-829, Aug. 1981.	
/TM/	5	KOLI et. al., "A fully differential class-AB switched-current integrator for signal processing," IEEE Journal of Solid-State Circuits, vol. 32, no. 2 , USA,	
		Feb. 1997, pp. 238 - 244.	
/TM/	6	MUHAMMAD et. al., "A Discrete-Time Bluetooth Receiver in a 0.13um Digital CMOS Process," Proceedings of the 2004 IEEE International Solid-State Circuits Conference, USA, pp. 268-269.	

Examiner Signature	/Tan Mai/	Date Considered	04/10/2007
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